

FIG. 1

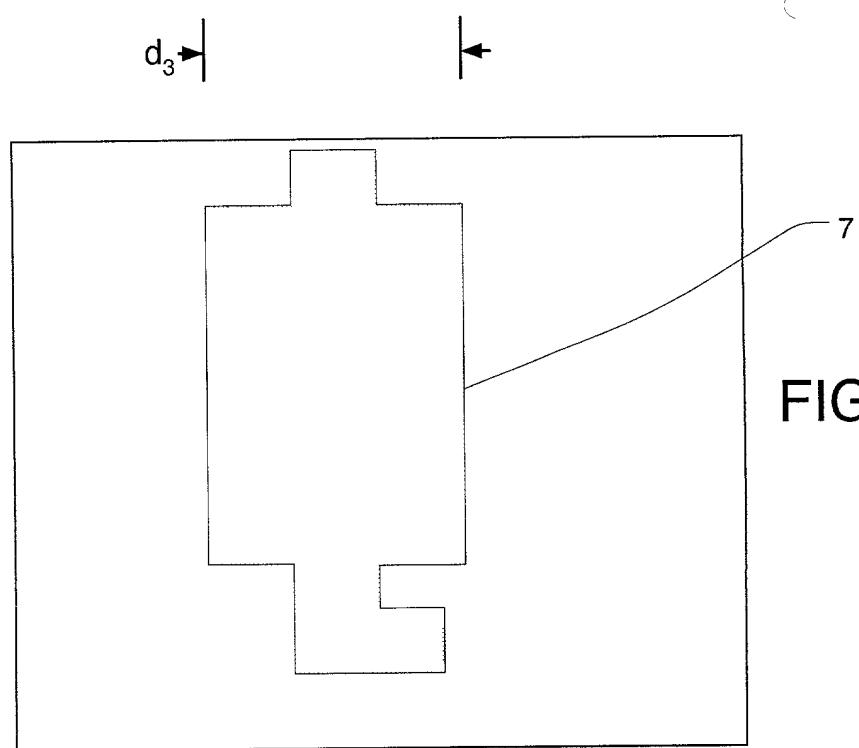


FIG. 2

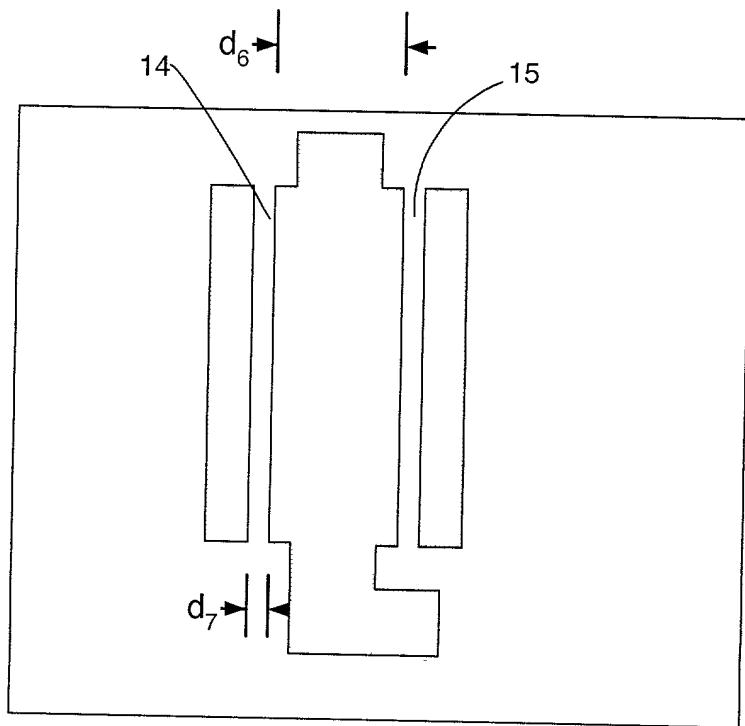
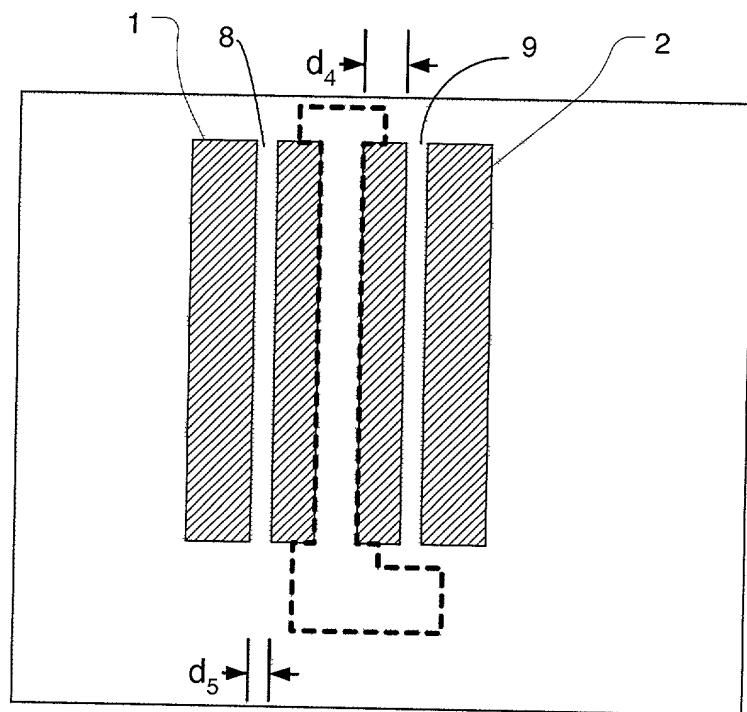


FIG. 4

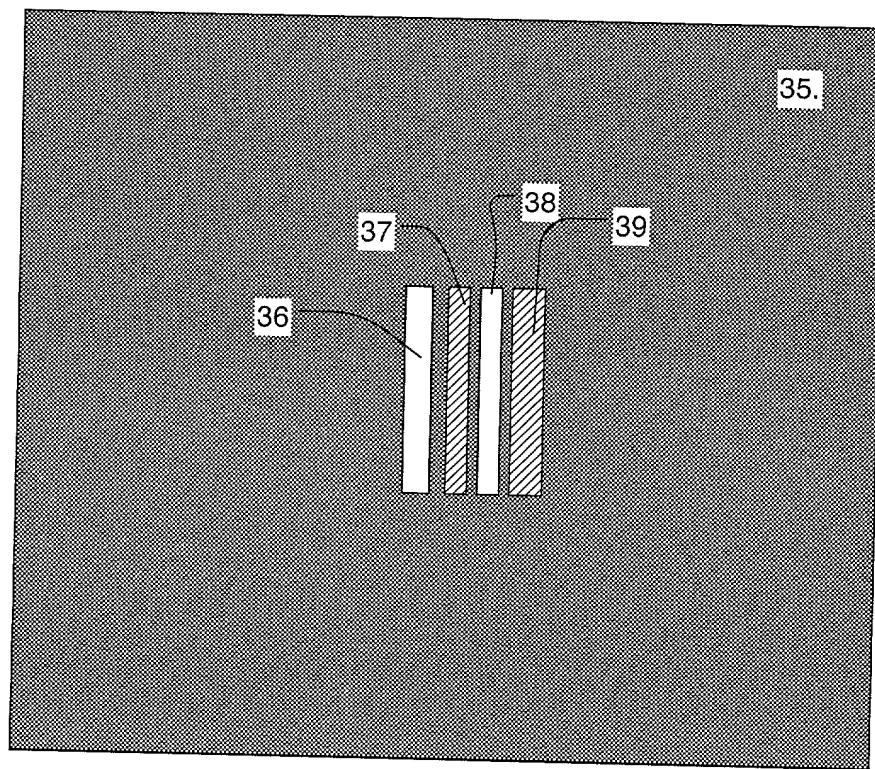
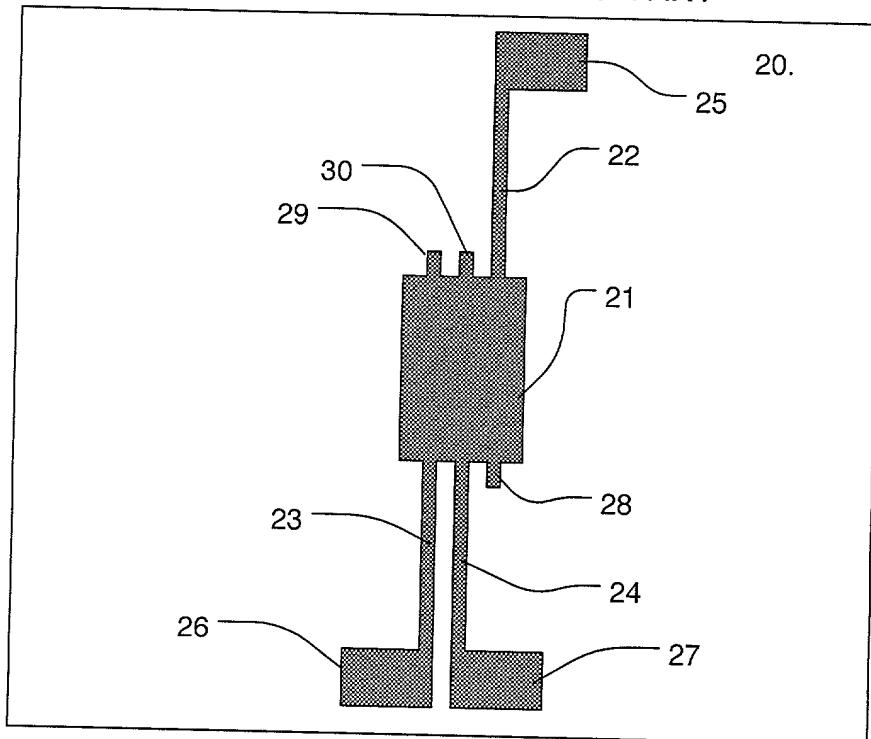
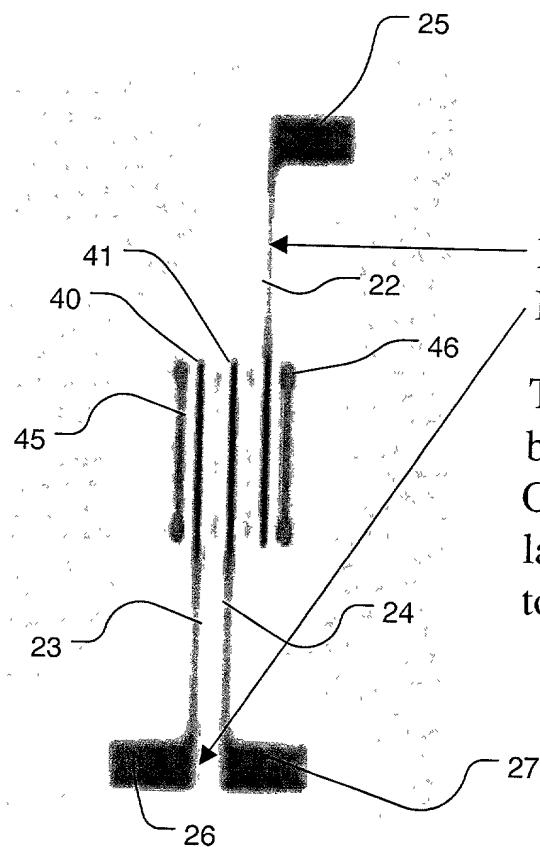
FIG. 5
PRIOR ARTFIG. 6
PRIOR ART



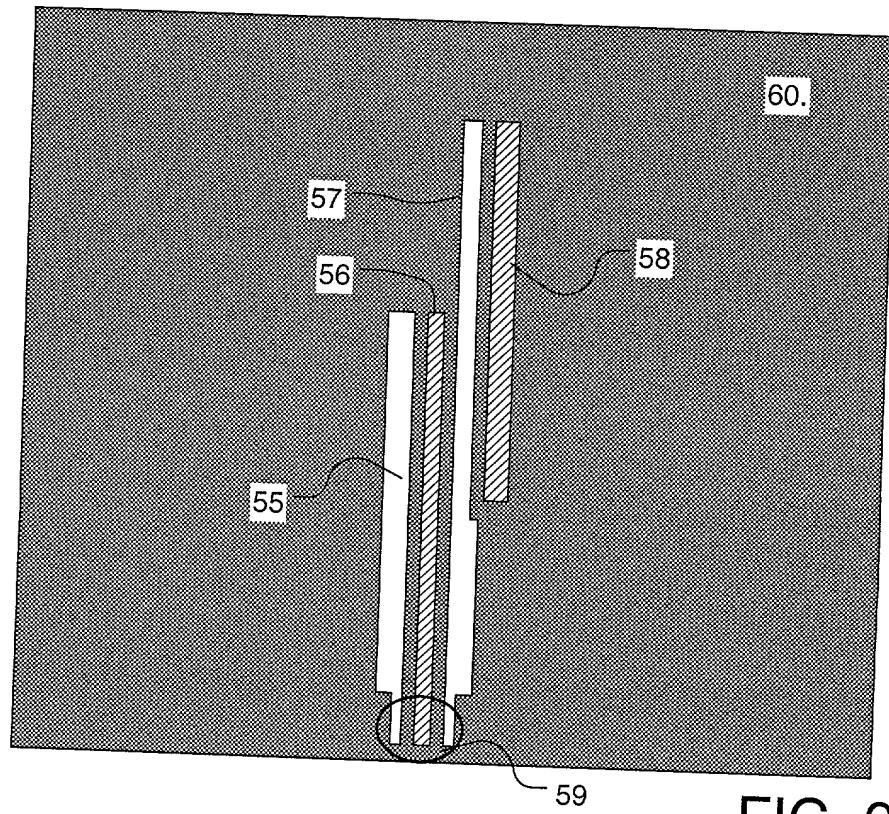
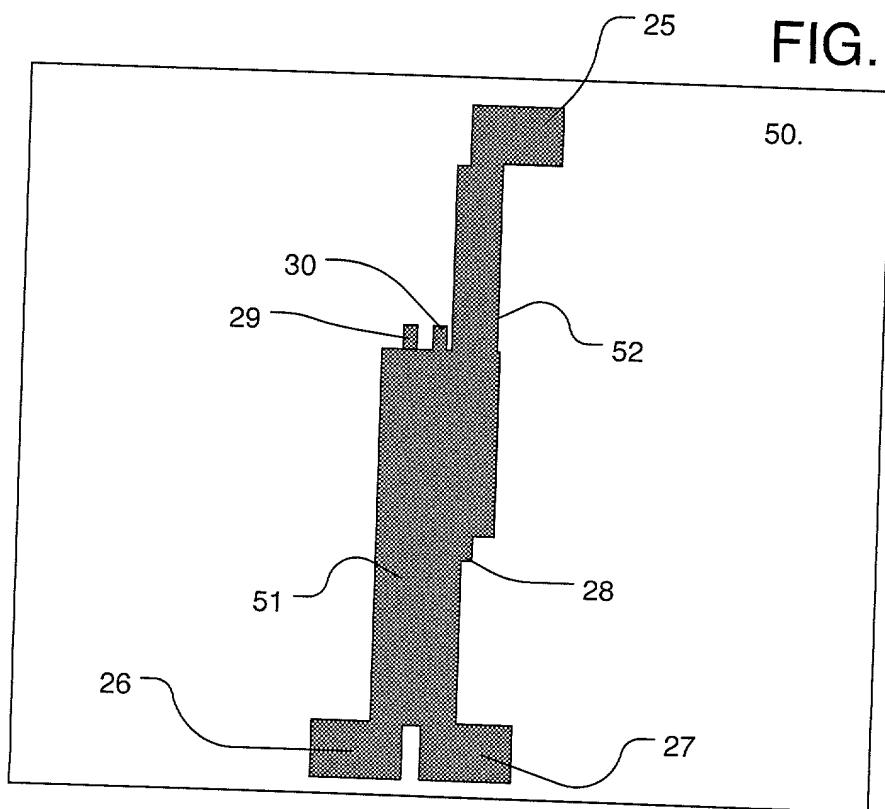
FIG. 7
PRIOR ART



Main issues:
Isolated lines
Isolated spaces

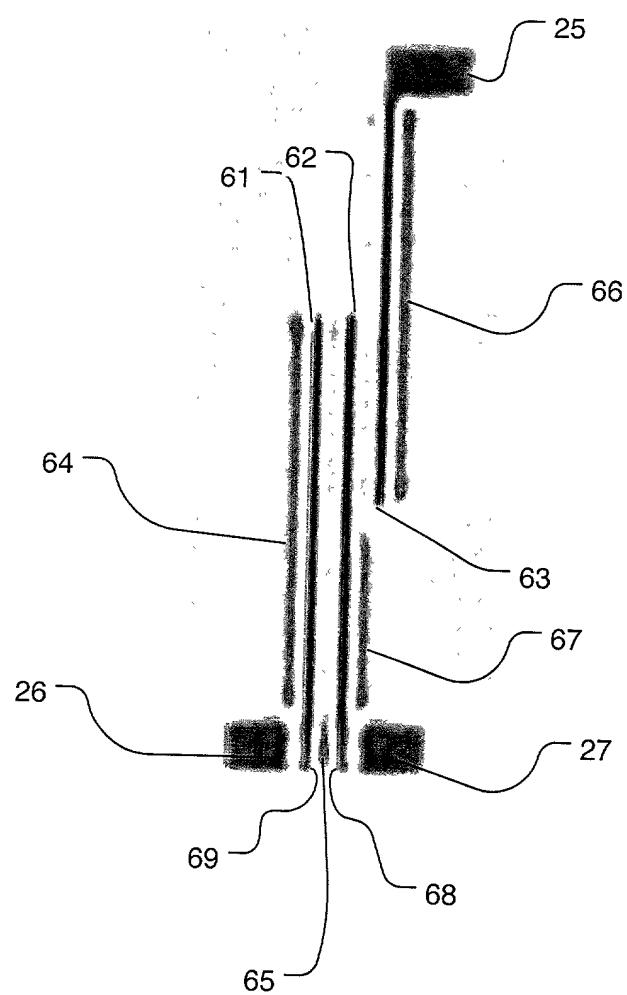
The CD issues can
be addressed using
OPC but the process
latitude will still be
too low





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FIG. 10



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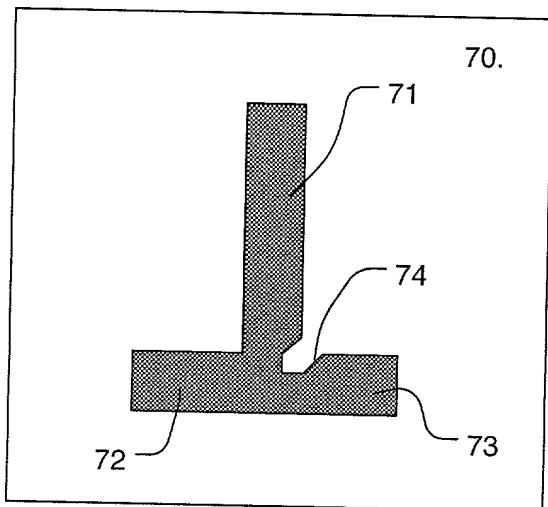


FIG. 11

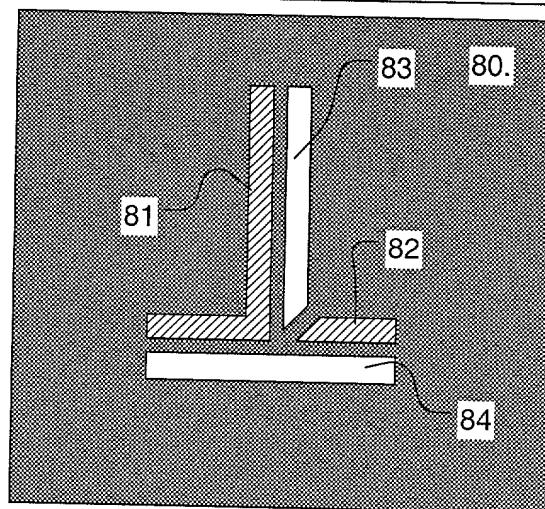


FIG. 12

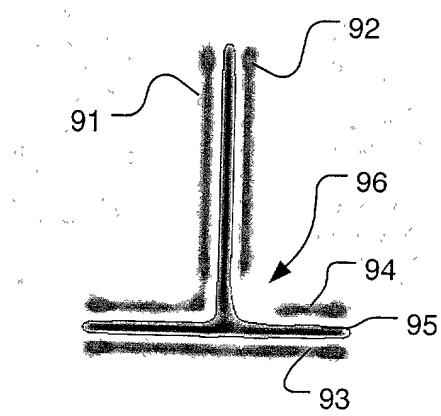


FIG. 13

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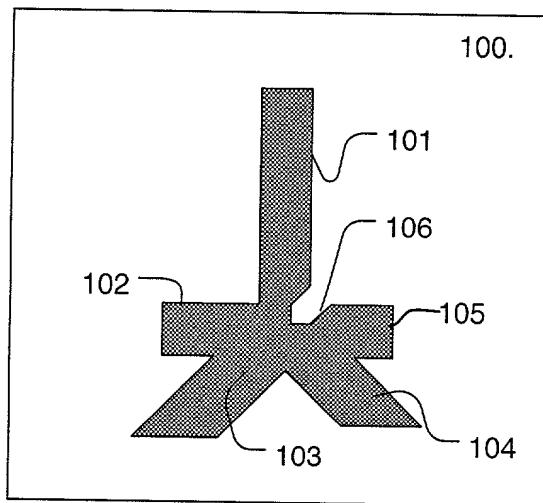


FIG. 14

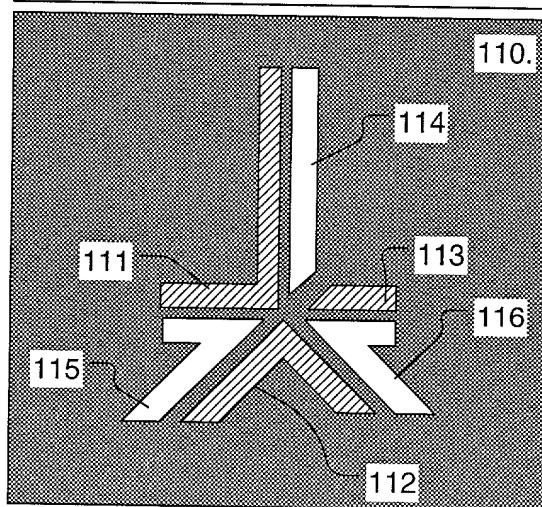


FIG. 15

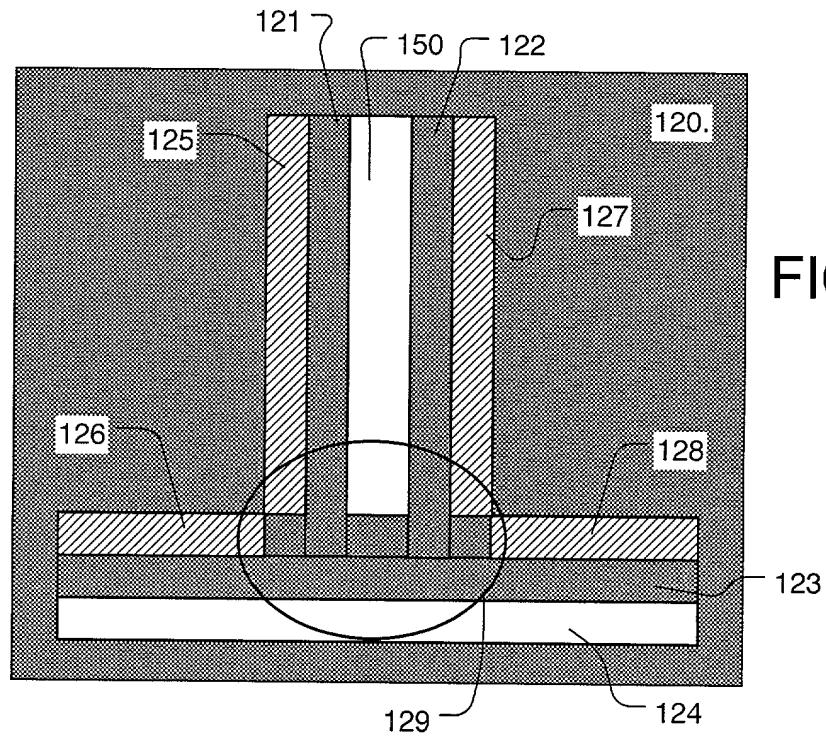


FIG. 16

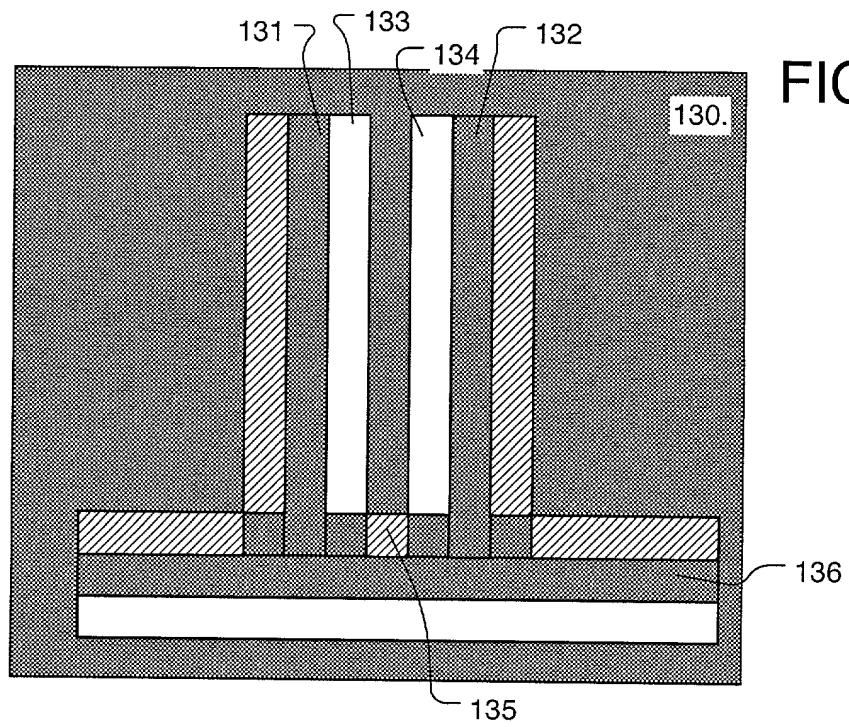


FIG. 17



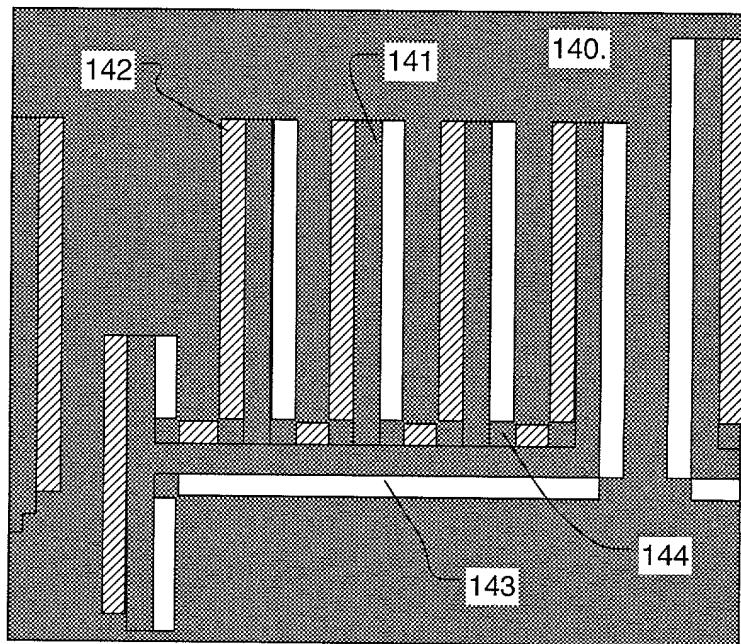


FIG. 18



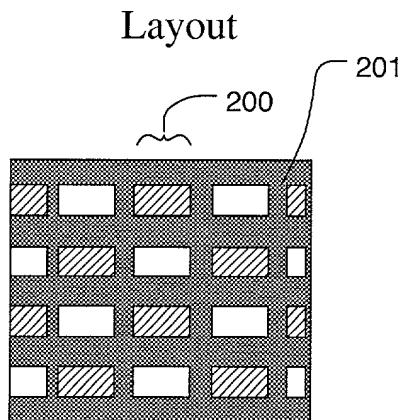


FIG. 19A

Simulation

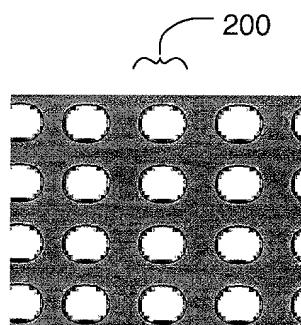


FIG. 19B

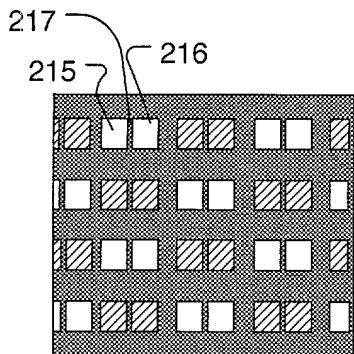


FIG. 20A

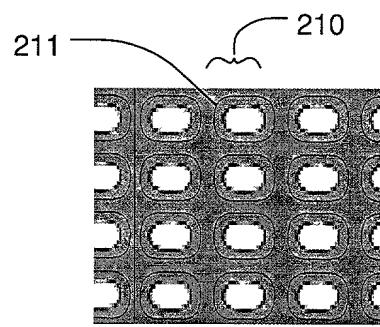


FIG. 20B

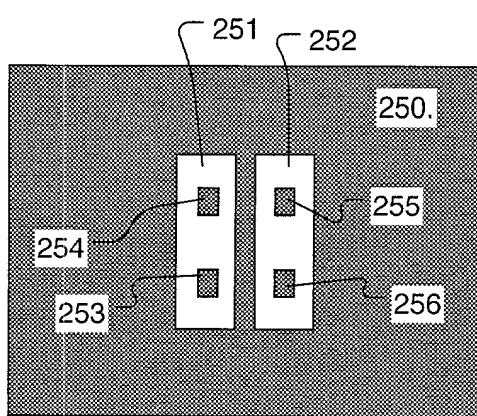


FIG. 21

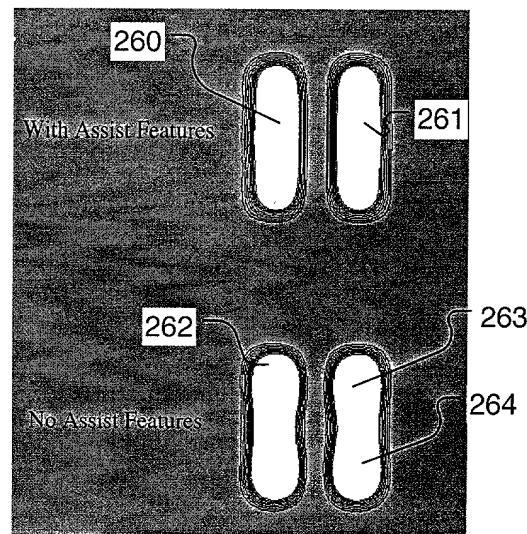


FIG. 22

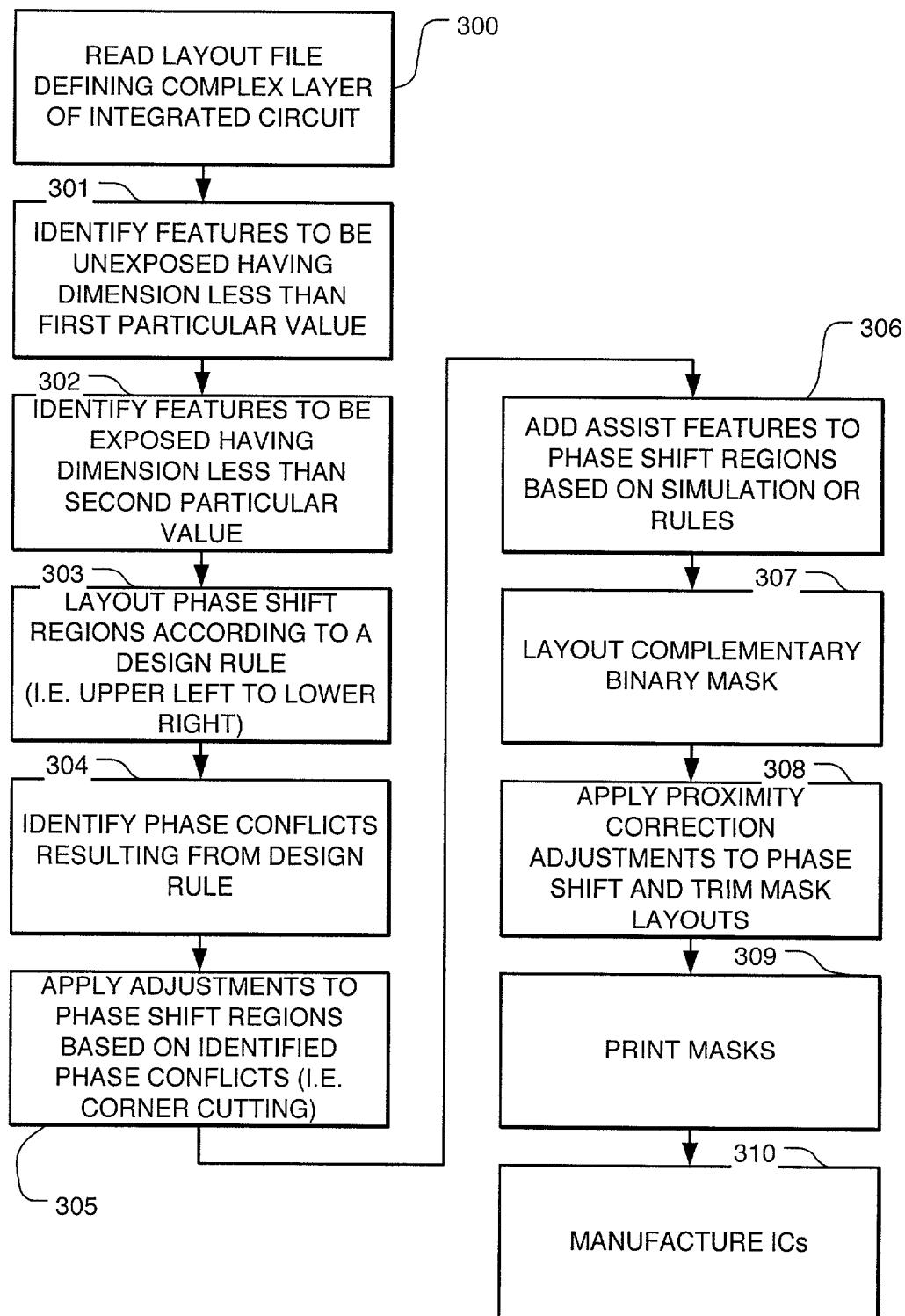


FIG. 23

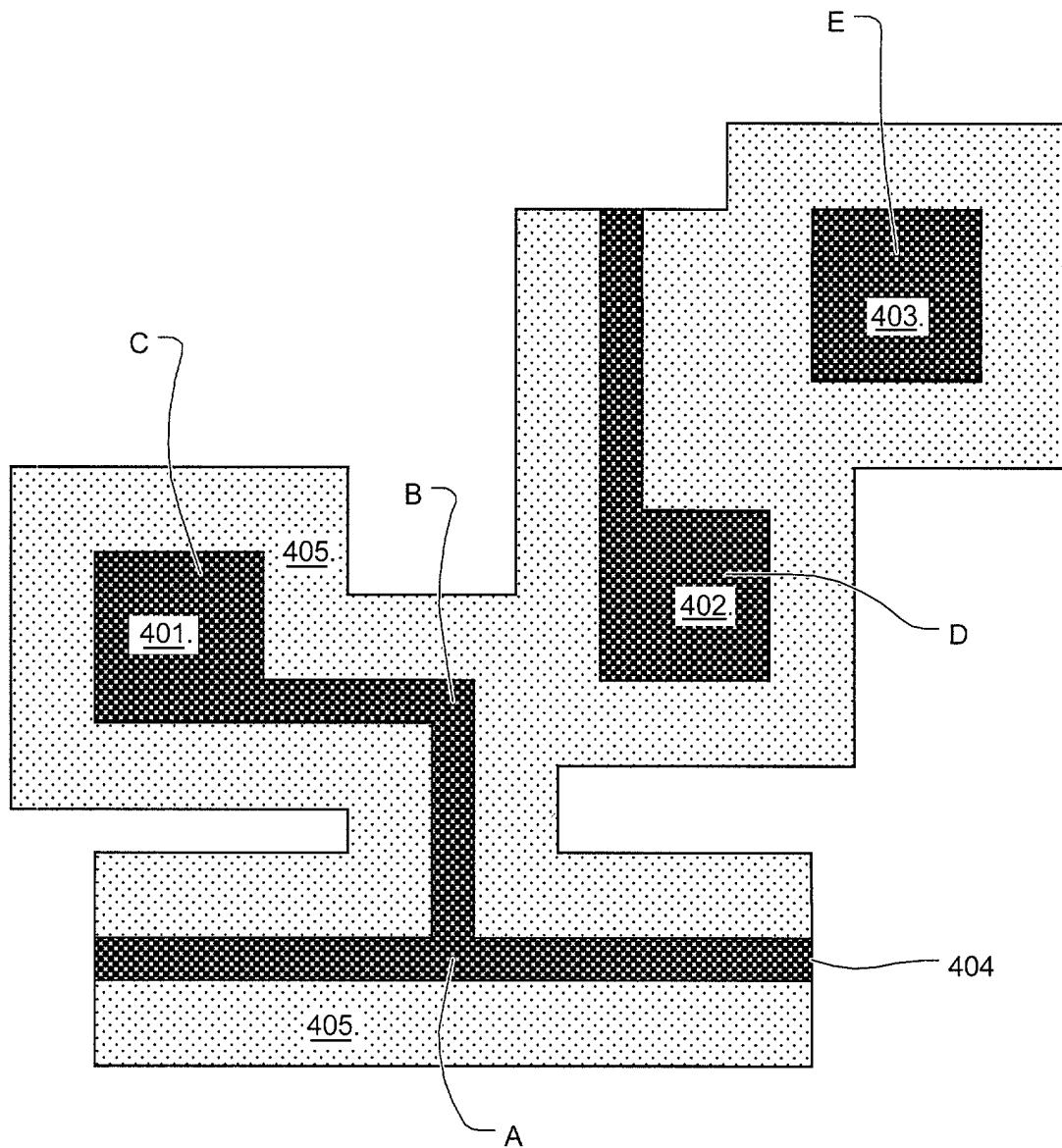


FIG. 24

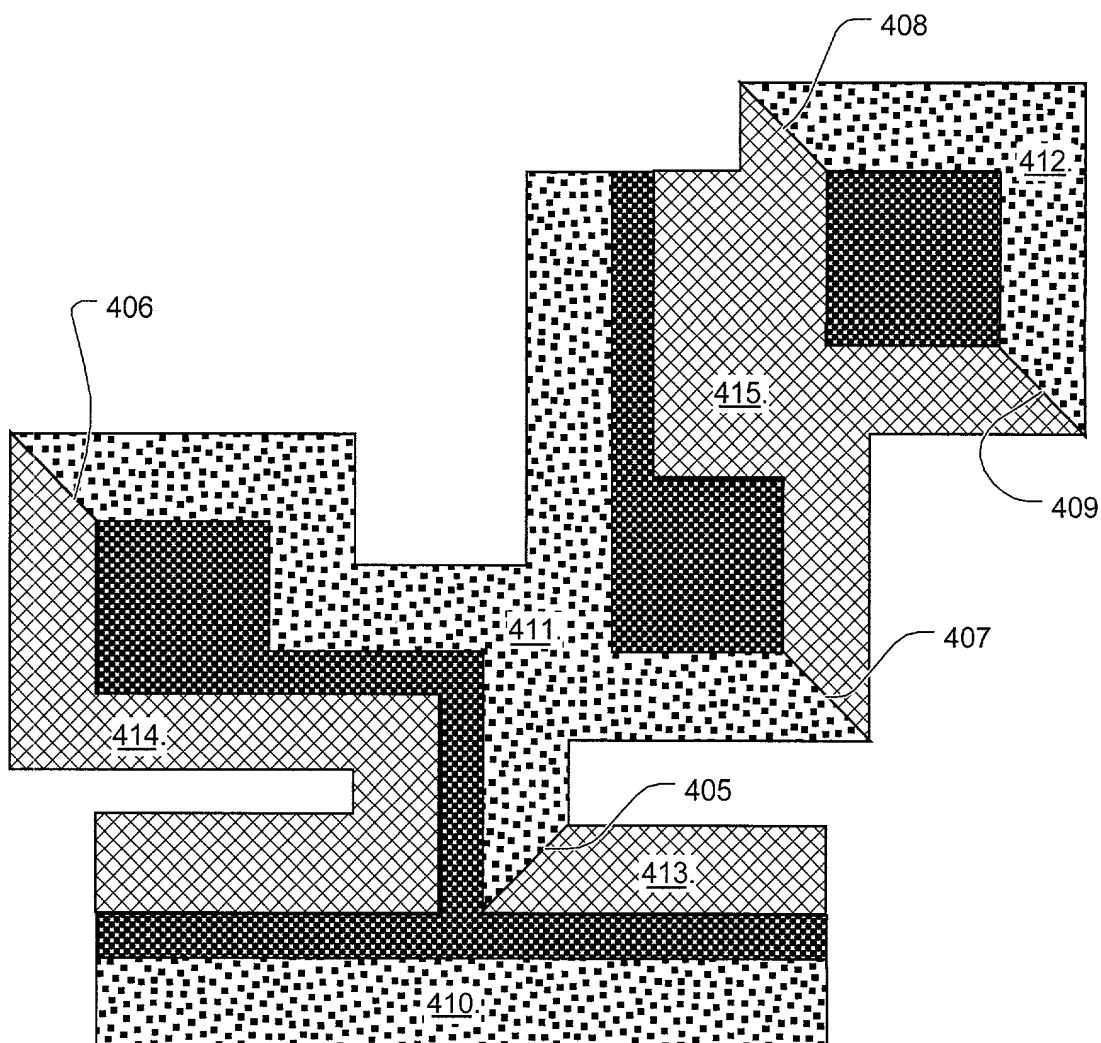
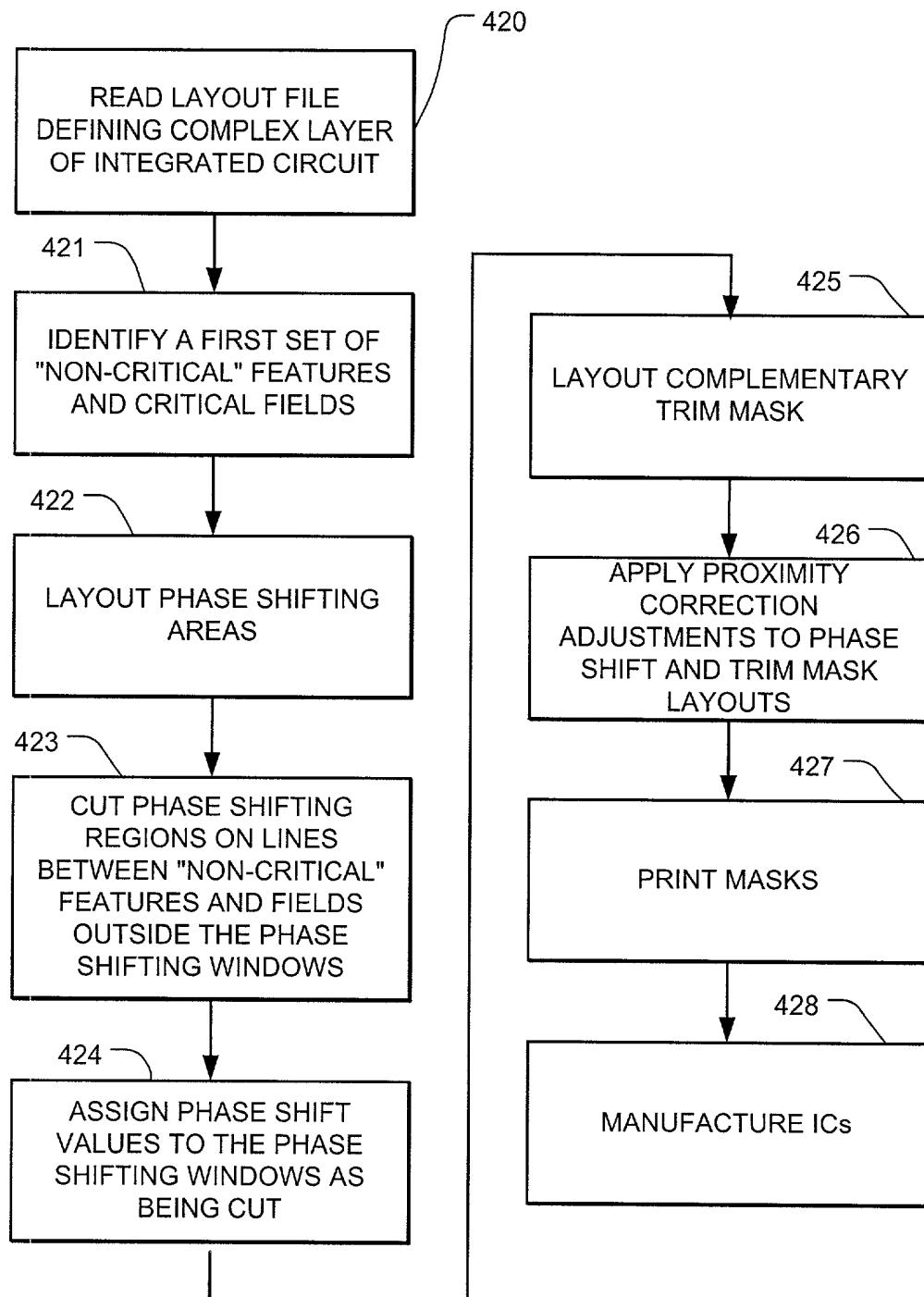


FIG. 25



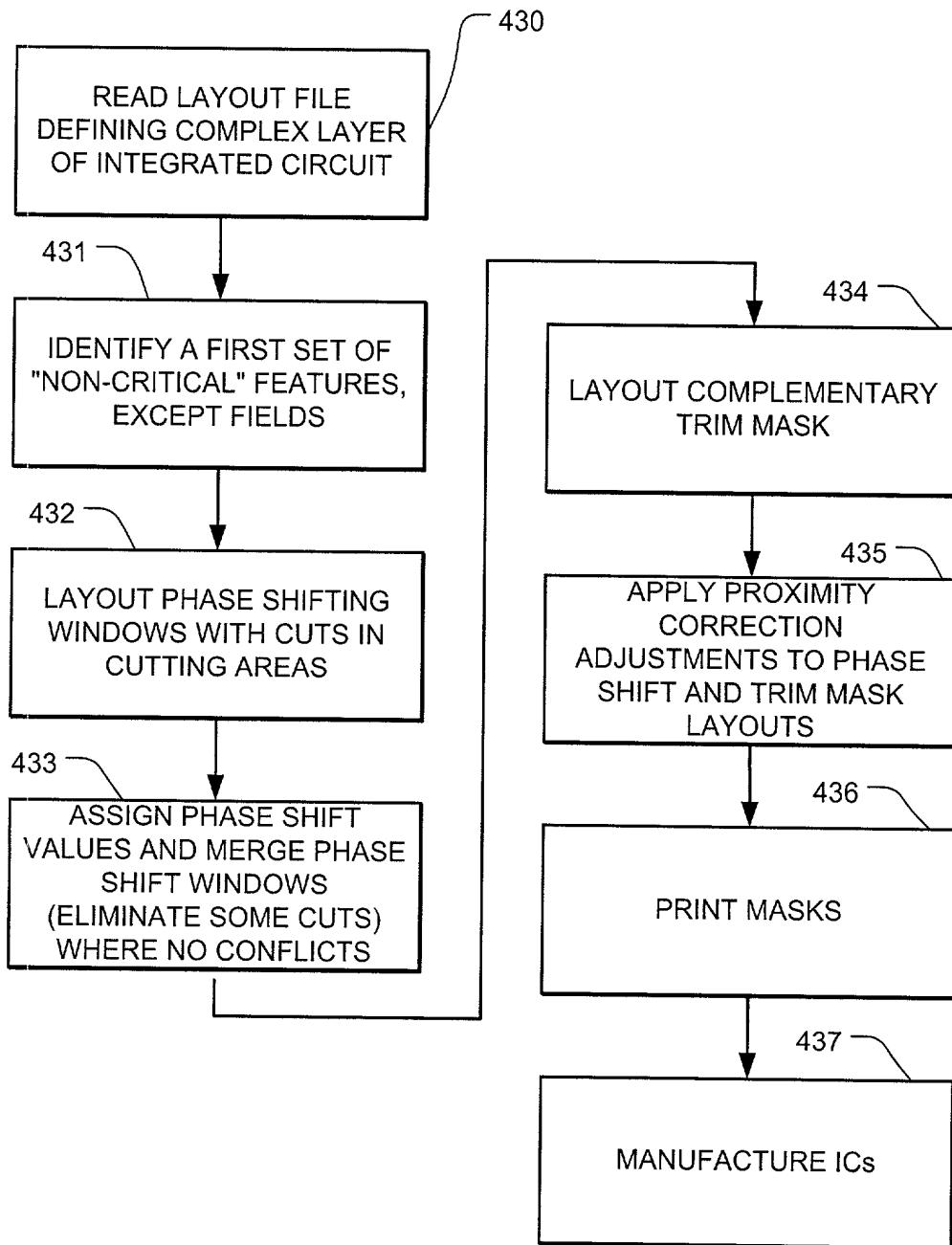


FIG. 27

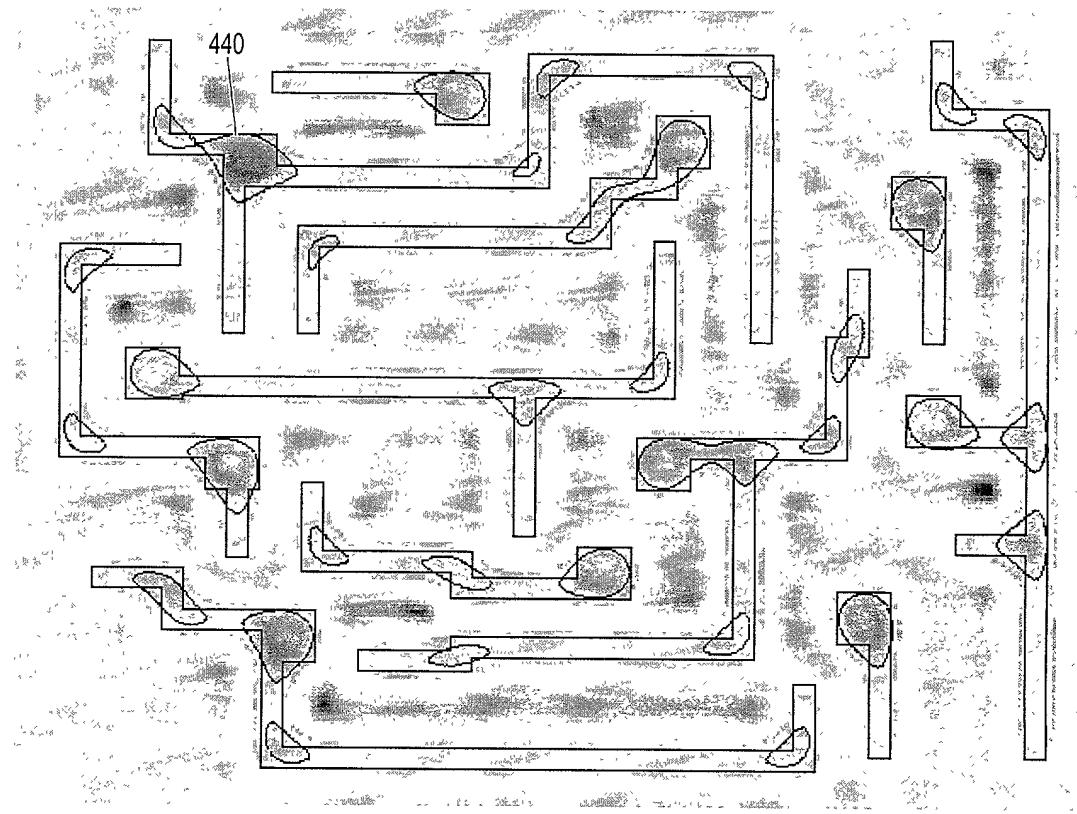
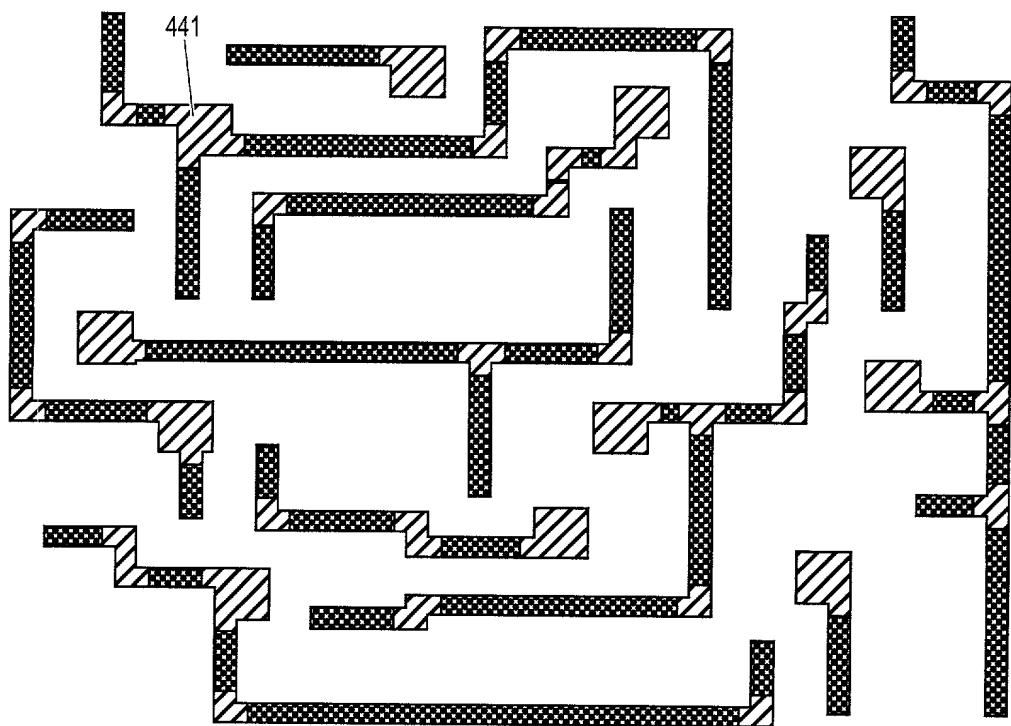


FIG. 28

FIG. 29



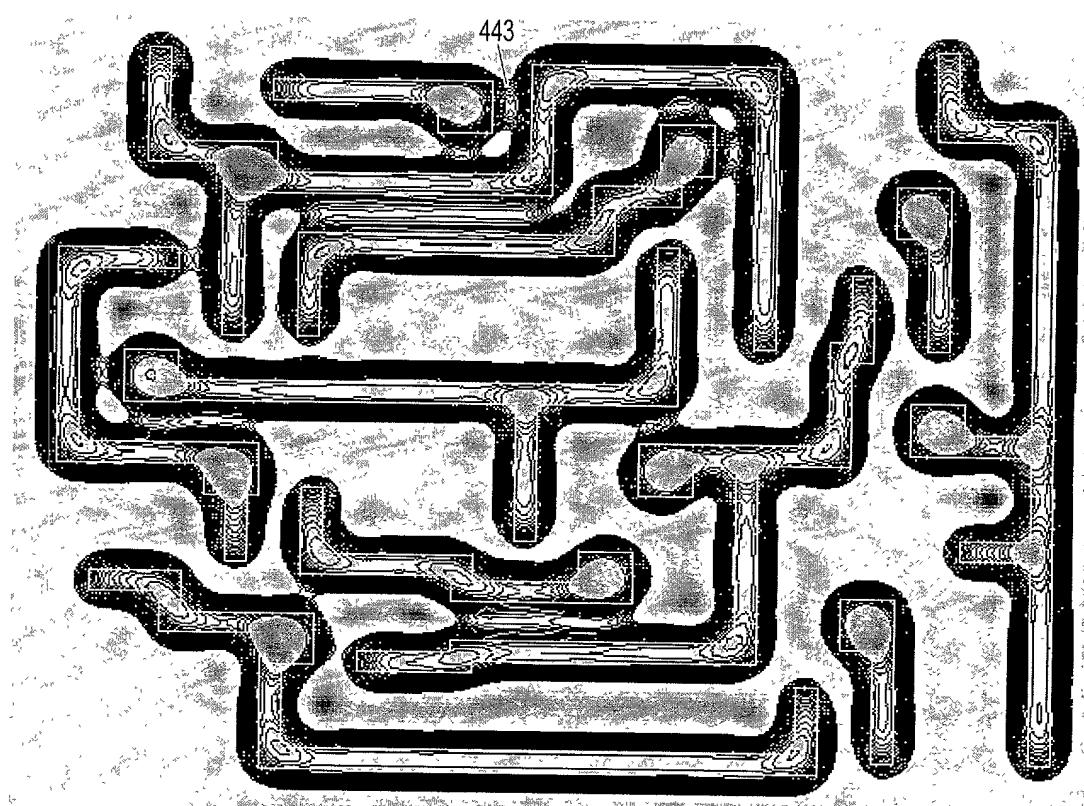
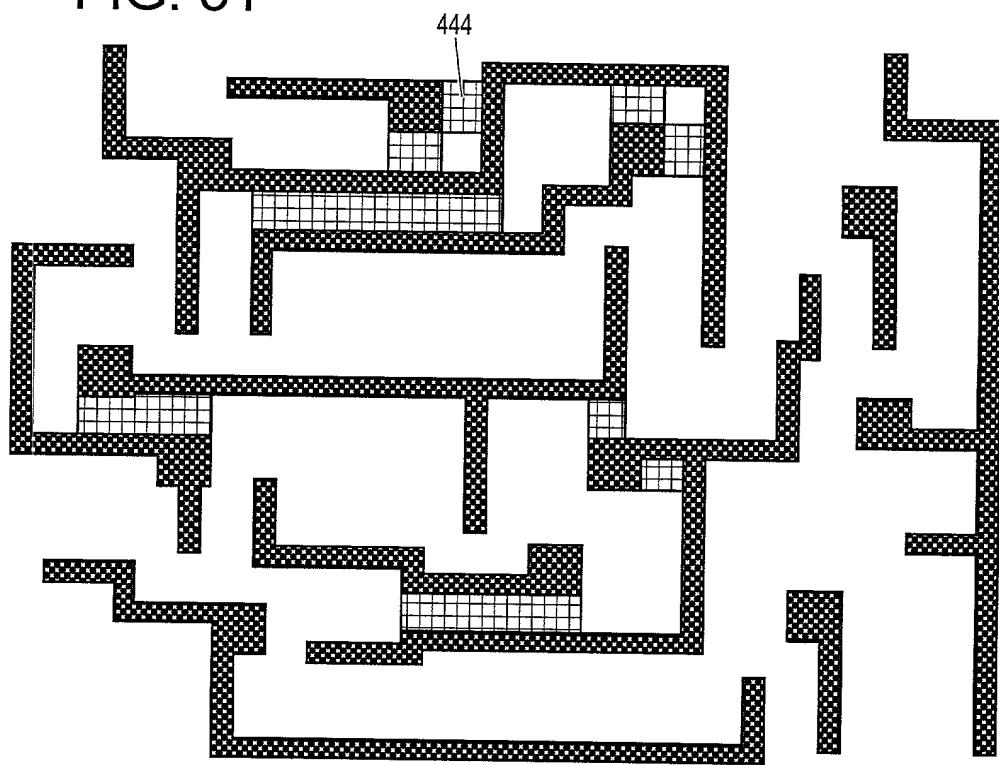


FIG. 31

FIG. 30



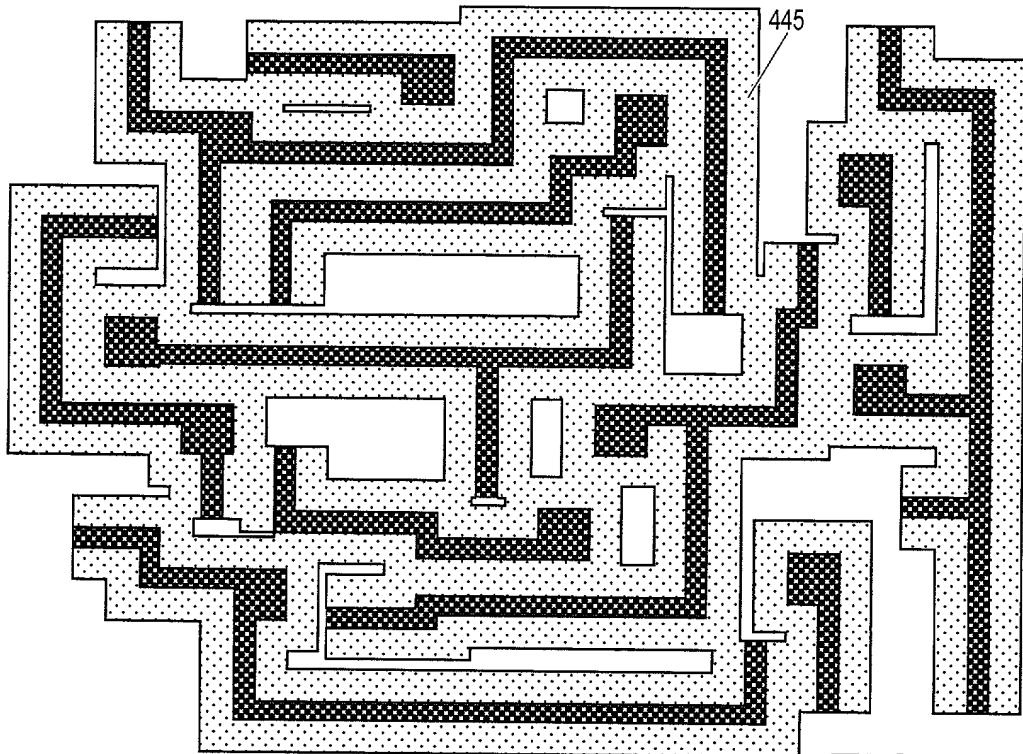
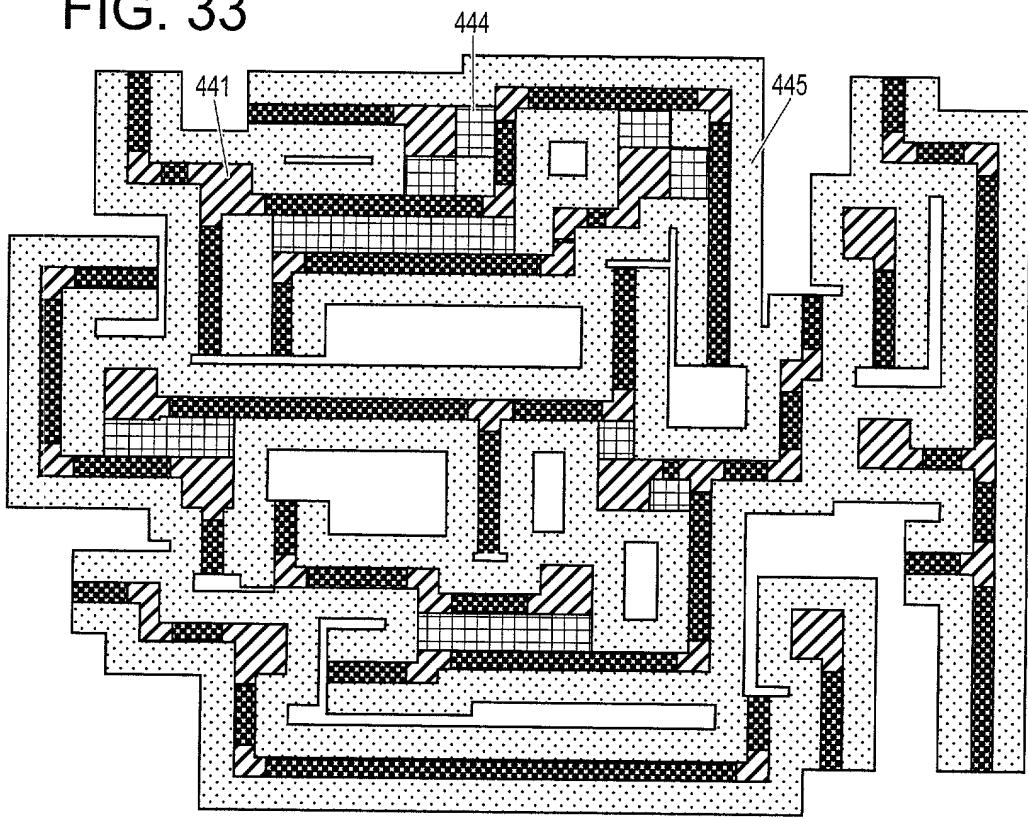


FIG. 32

FIG. 33



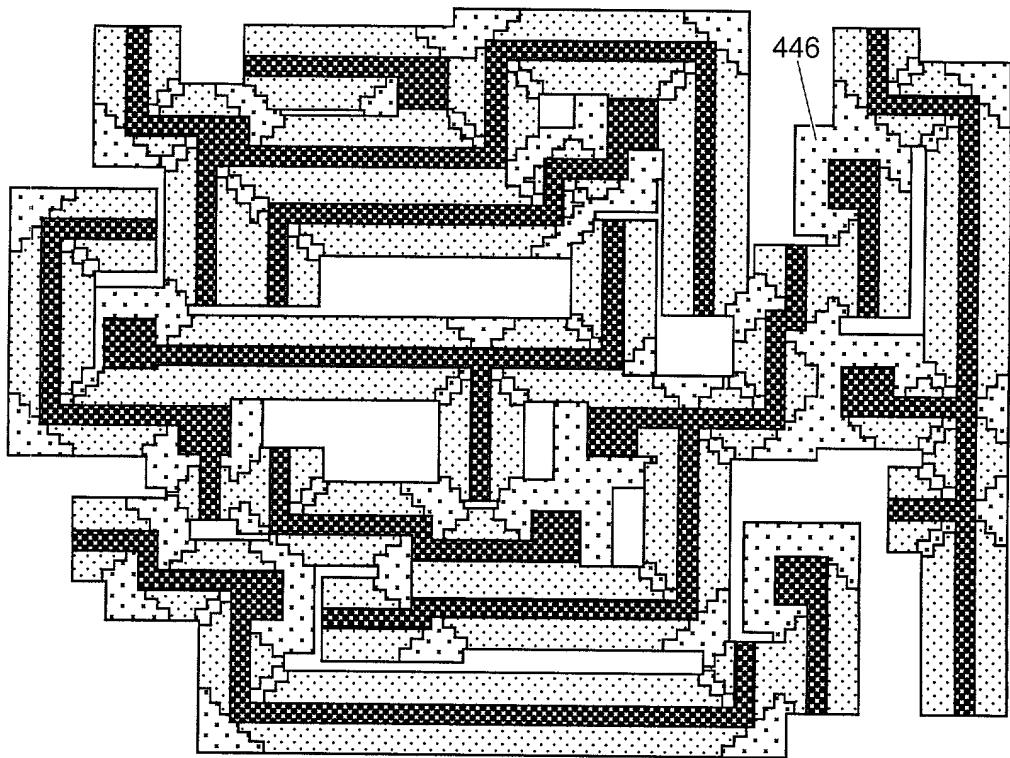
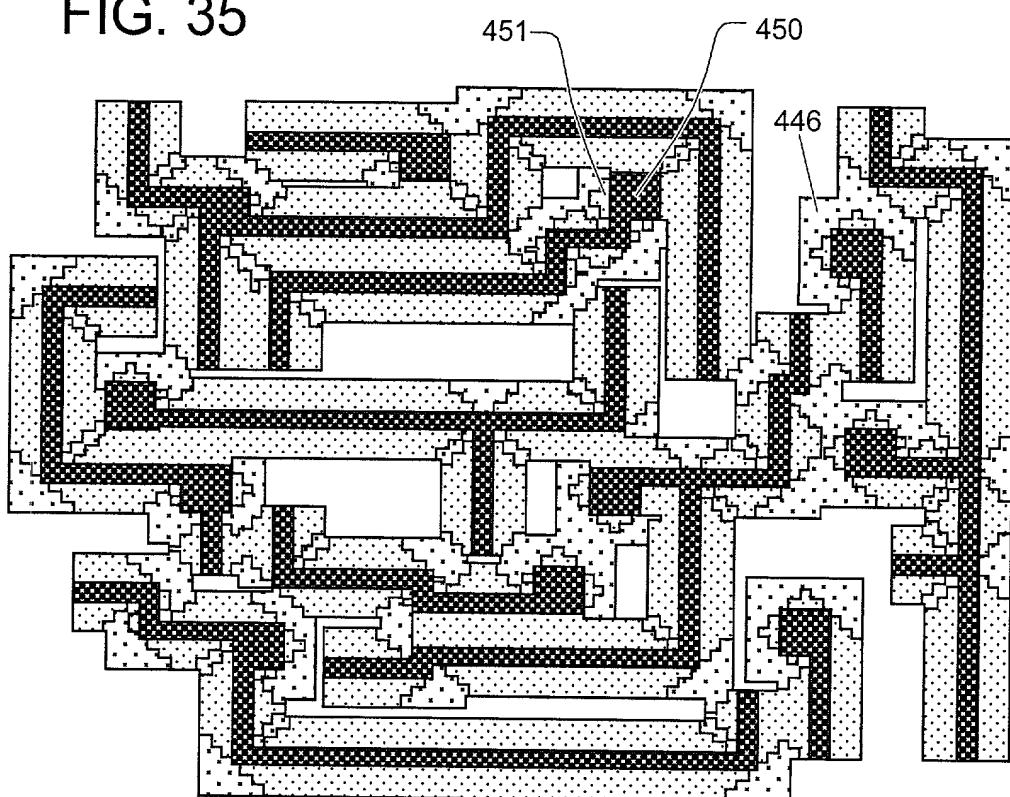


FIG. 34

FIG. 35



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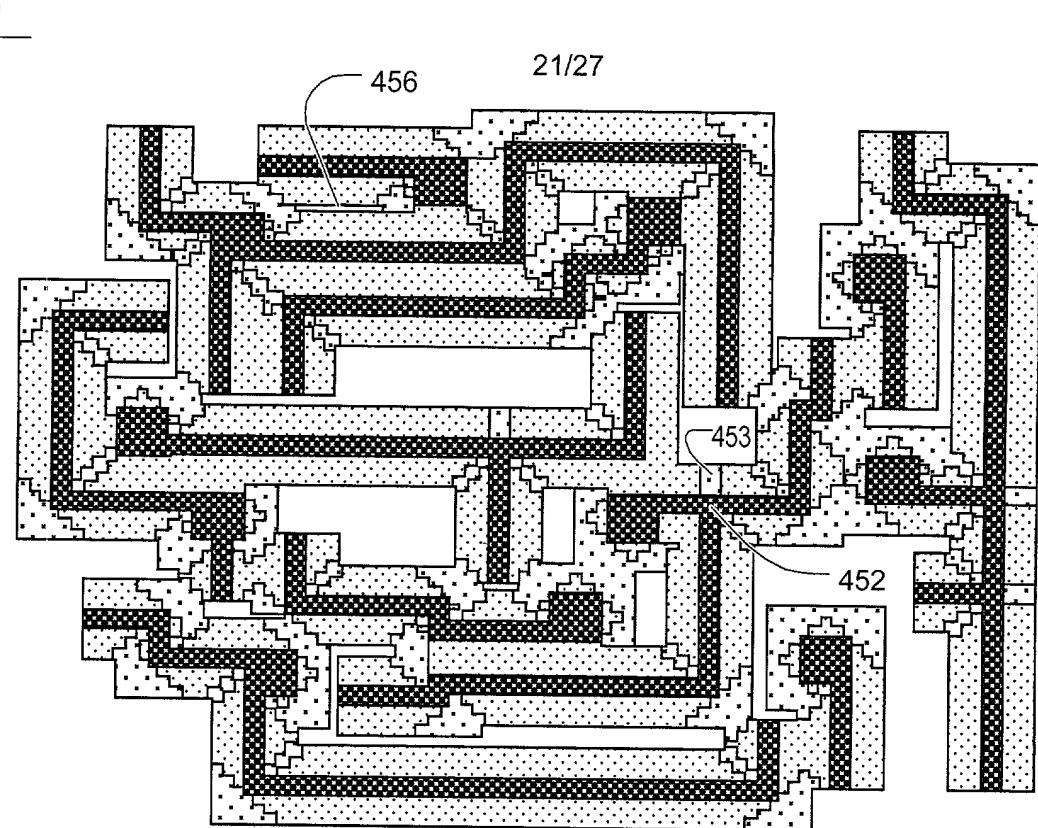
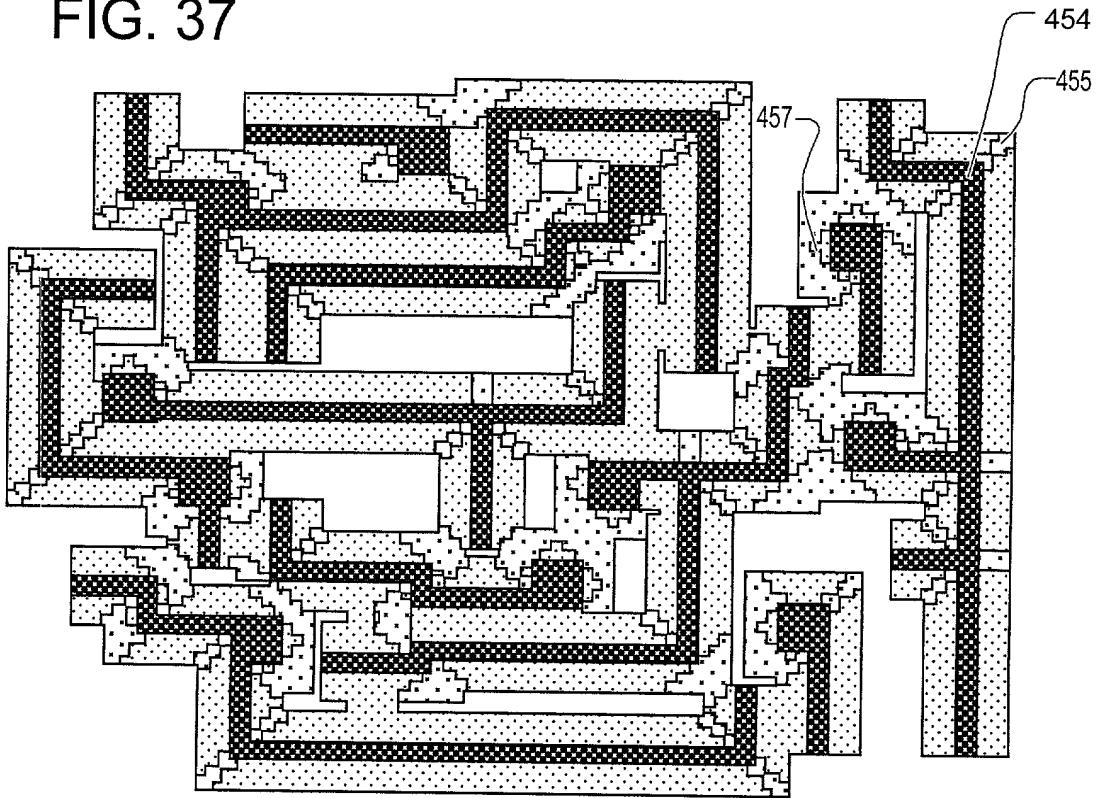


FIG. 36

FIG. 37



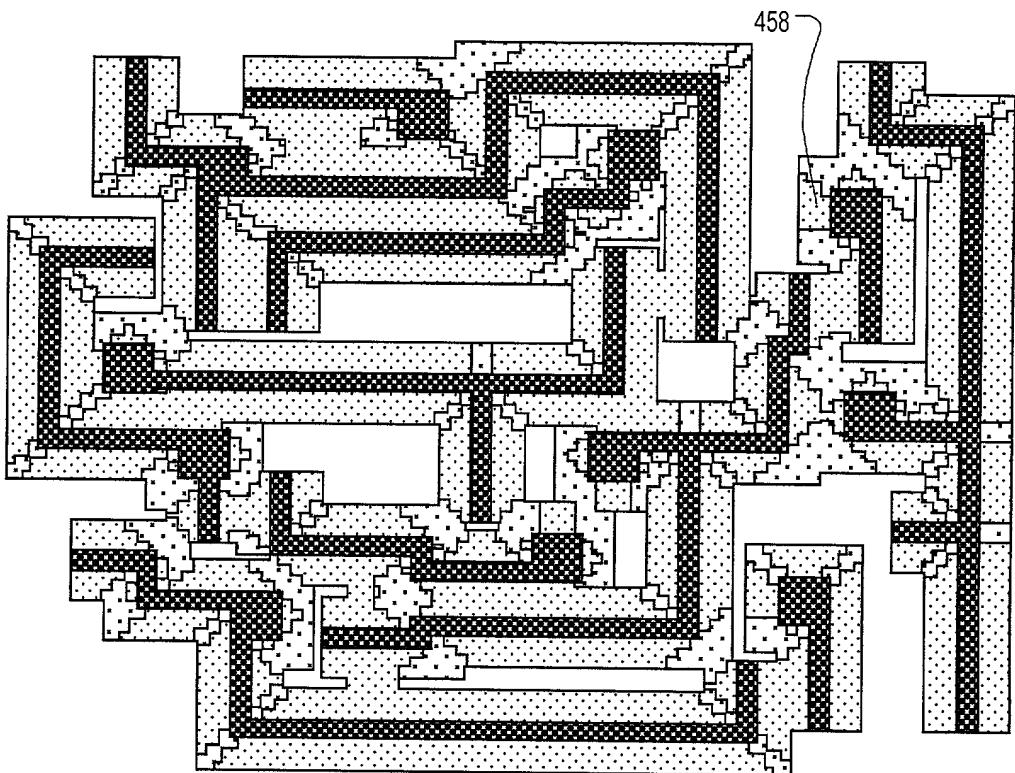


FIG. 38





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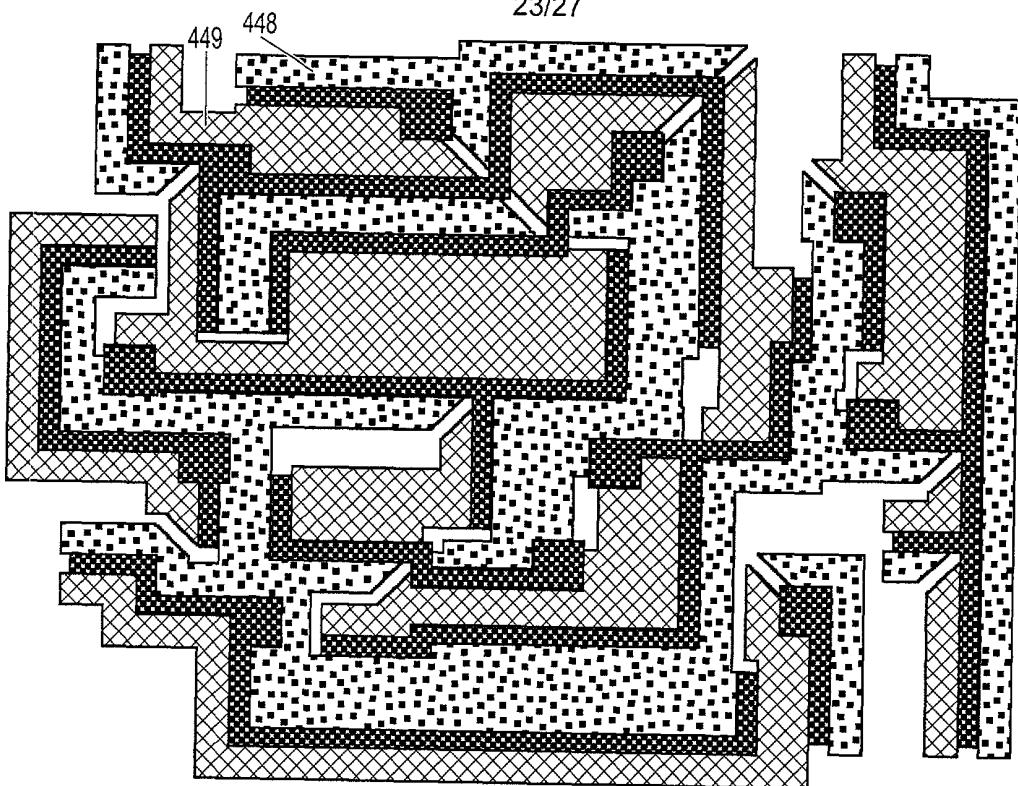


FIG. 39

FIG. 40

1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100

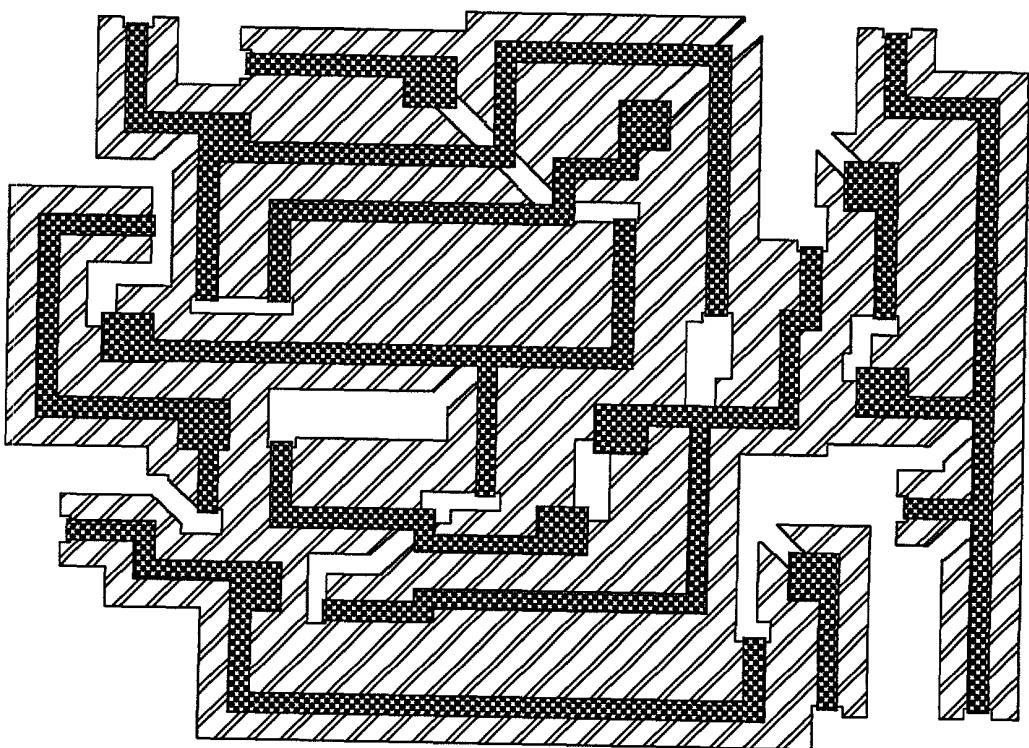
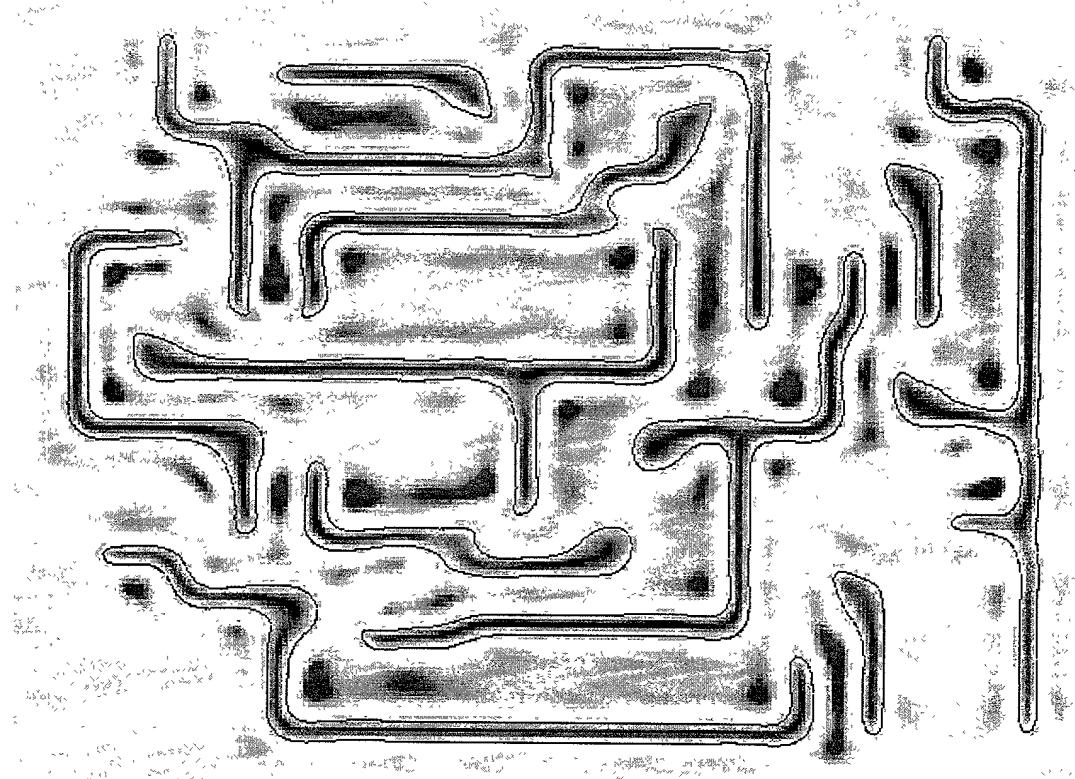


FIG. 41



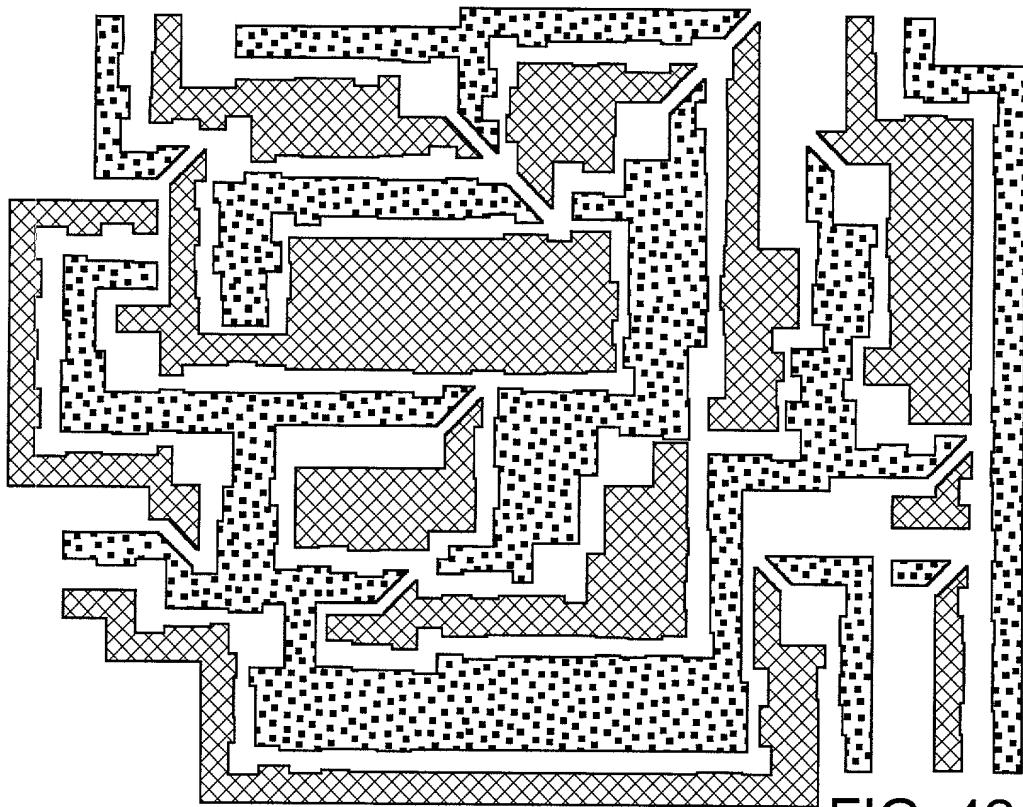
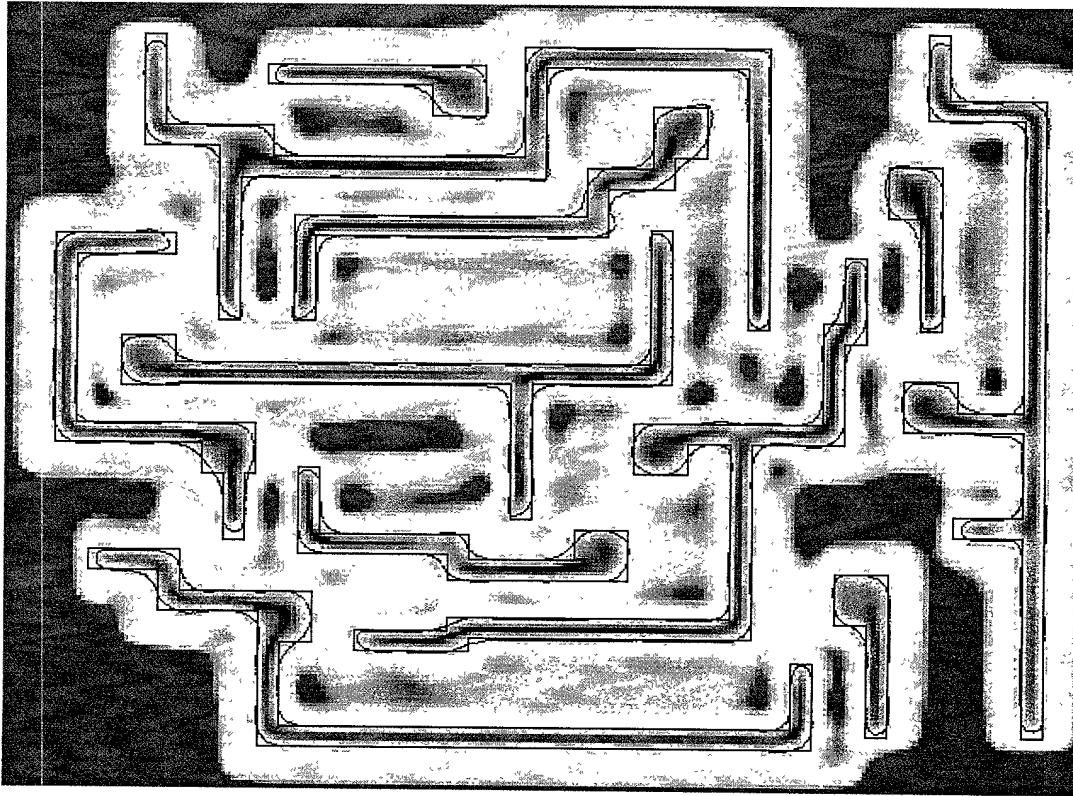


FIG. 42

FIG. 43



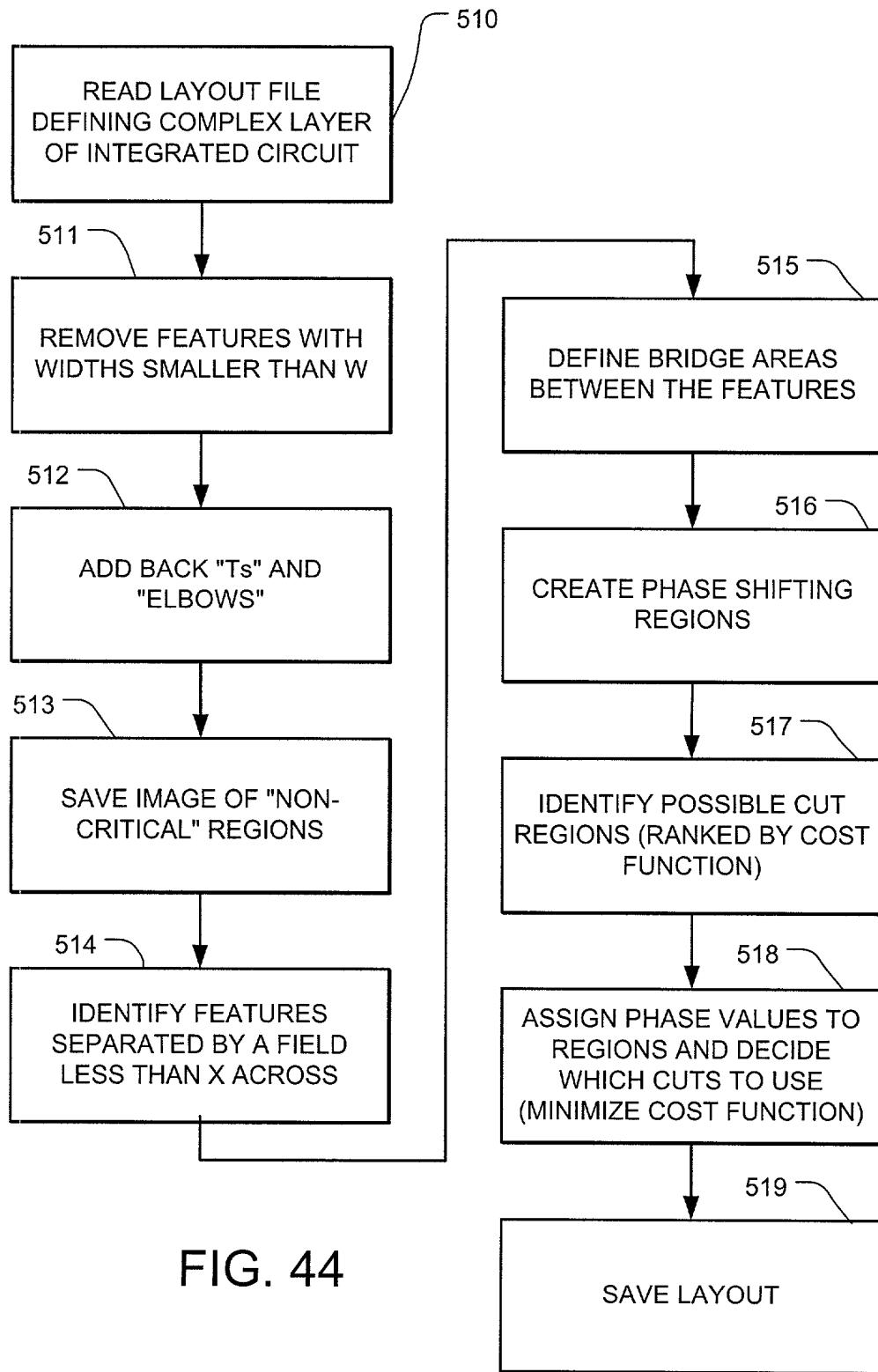


FIG. 44

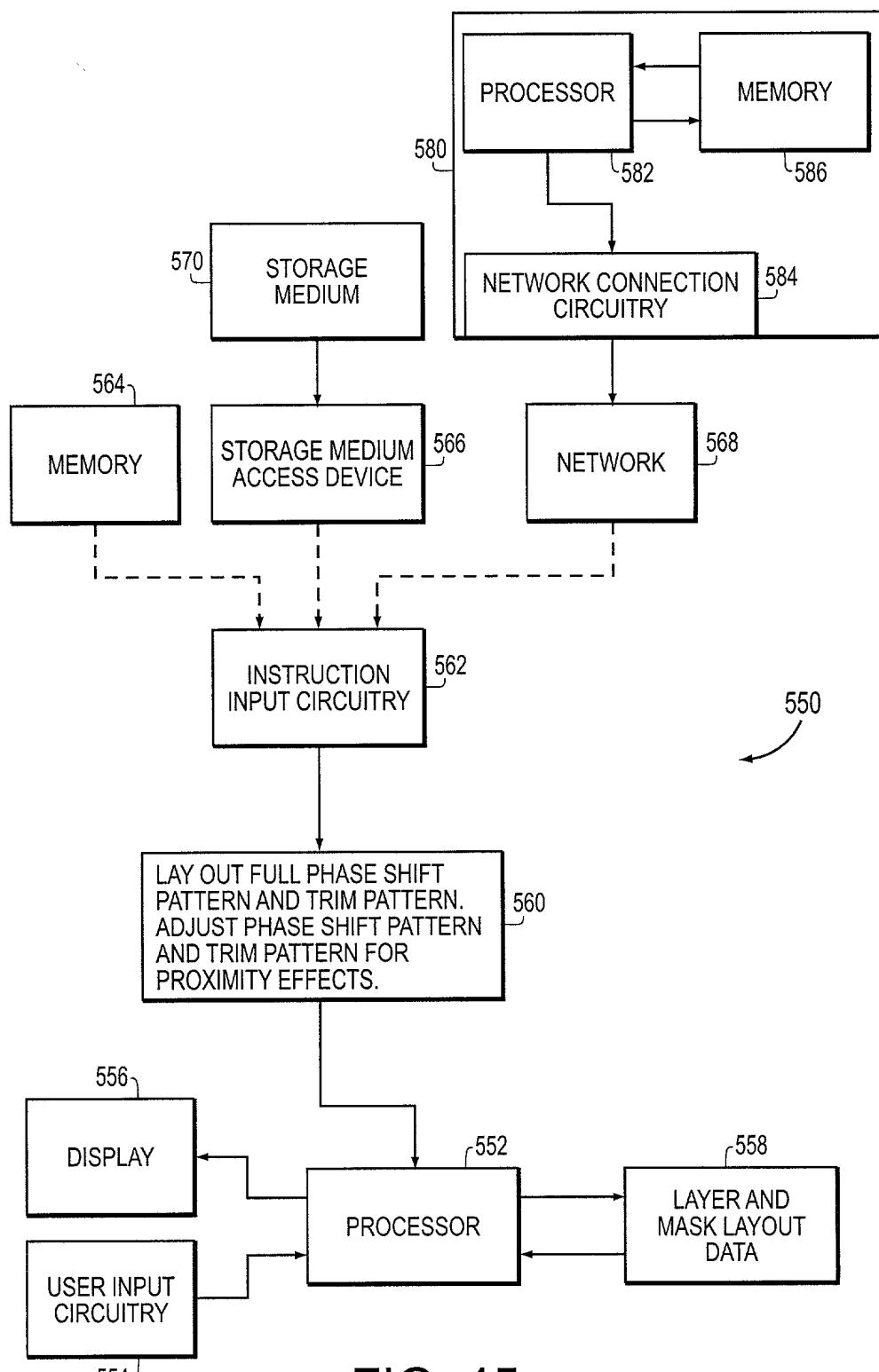


FIG. 45